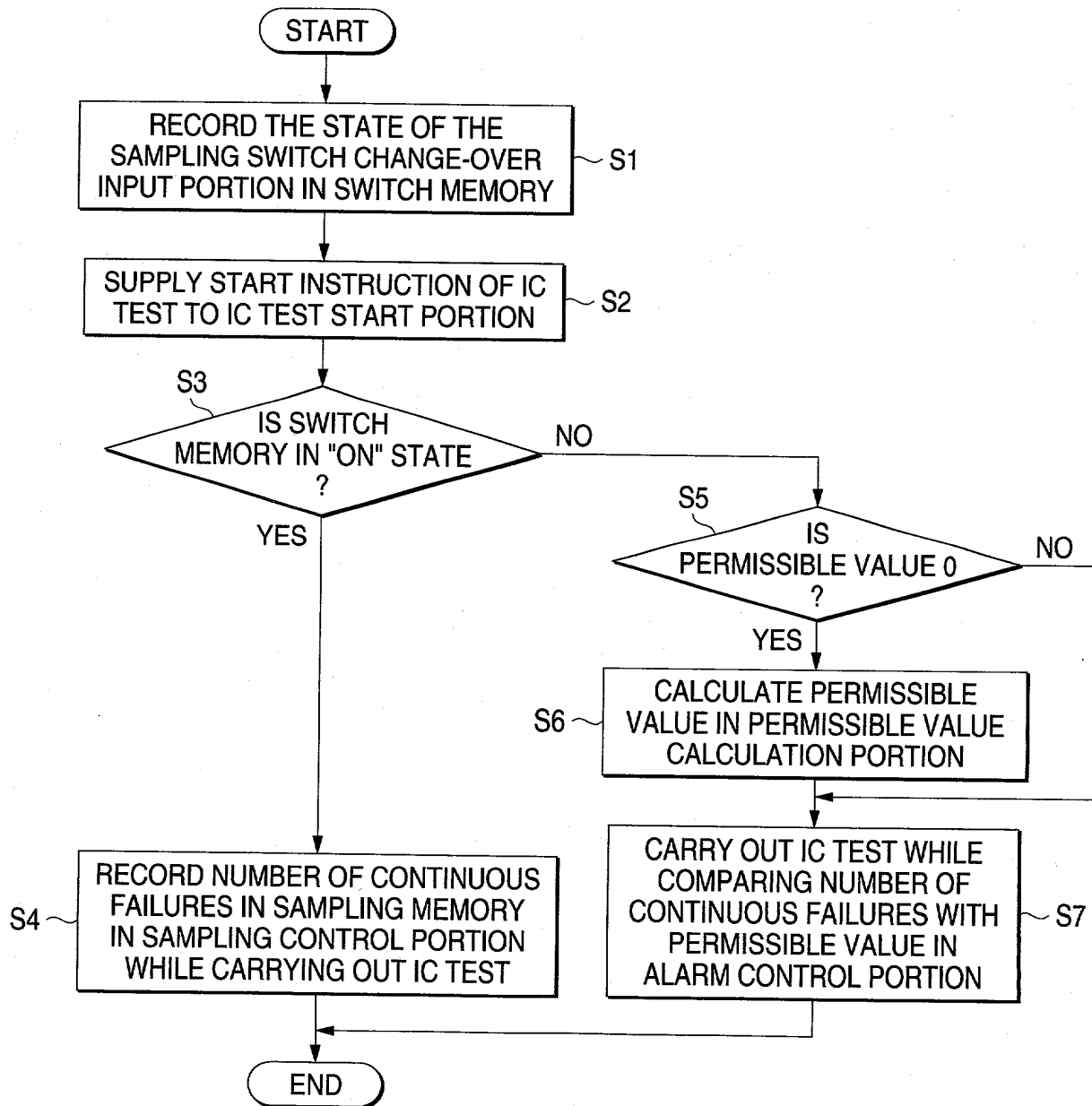


The diagram illustrates a sampling control system (2) and its connections. The system (2) is composed of several interconnected components:

- Input Unit (1):** Contains a **SAMPLING SWITCH CHANGE-OVER INPUT PORTION (1A)** and a **START INPUT PORTION (1B)**. It provides input to the **SAMPLING CONTROL PORTION (2D)** within system (2).
- System (2) Internal Components:**
 - SWITCH MEMORY (2A)** is connected to the **IC TEST START PORTION (2B)**.
 - The **IC TEST START PORTION (2B)** is connected to the **SAMPLING CONTROL PORTION (2D)**.
 - A dashed box labeled **2C** contains three memory blocks: **SAMPLING MEMORY (2C1)**, **PERMISSIBLE VALUE MEMORY (2C2)**, and **MEASURED VALUE MEMORY (2C3)**.
 - The **SAMPLING CONTROL PORTION (2D)** is bidirectionally connected to the **SAMPLING MEMORY (2C1)**.
 - The **SAMPLING CONTROL PORTION (2D)** is connected to the **PERMISSIBLE VALUE CALCULATION PORTION (2E)**.
 - The **PERMISSIBLE VALUE CALCULATION PORTION (2E)** is bidirectionally connected to the **PERMISSIBLE VALUE MEMORY (2C2)**.
 - The **PERMISSIBLE VALUE CALCULATION PORTION (2E)** is connected to the **ALARM CONTROL PORTION (2F)**.
 - The **ALARM CONTROL PORTION (2F)** is bidirectionally connected to the **MEASURED VALUE MEMORY (2C3)**.
- Output Unit (3):** Contains an **IC TEST PORTION (3A)**, which receives data from the **SAMPLING CONTROL PORTION (2D)**.
- Alarm Unit (4):** Contains an **ALARM DISPLAY PORTION (4A)**, which receives data from the **ALARM CONTROL PORTION (2F)**.

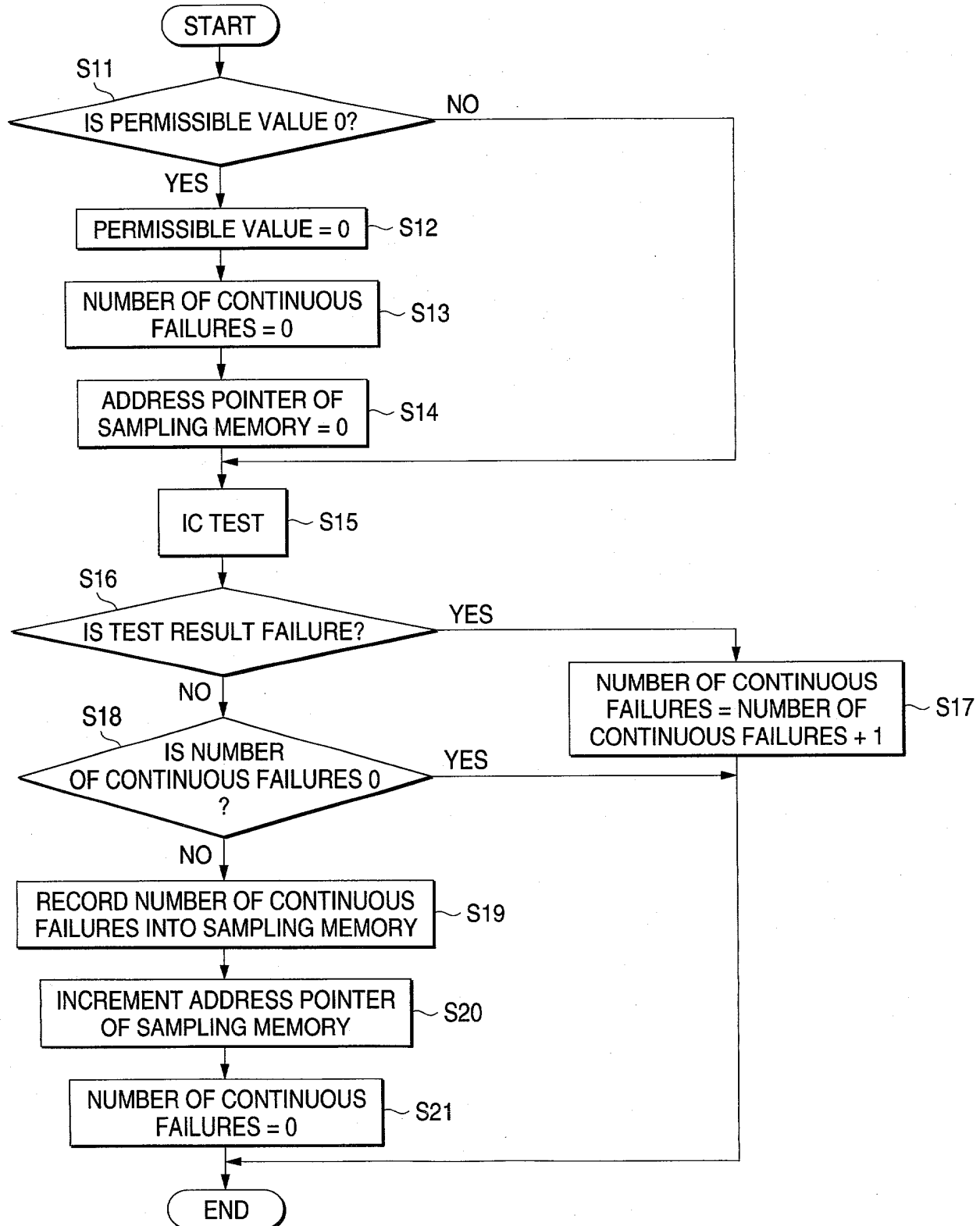
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FIG. 2



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FIG. 3



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FIG. 4

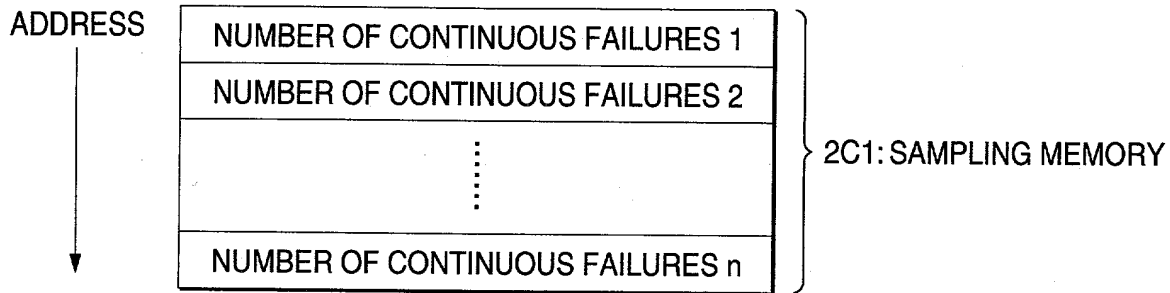
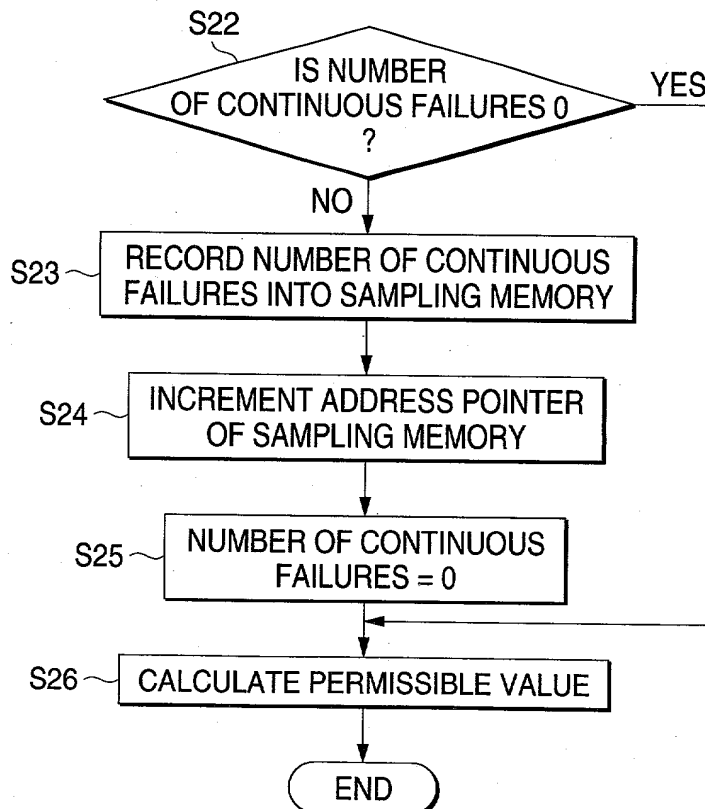
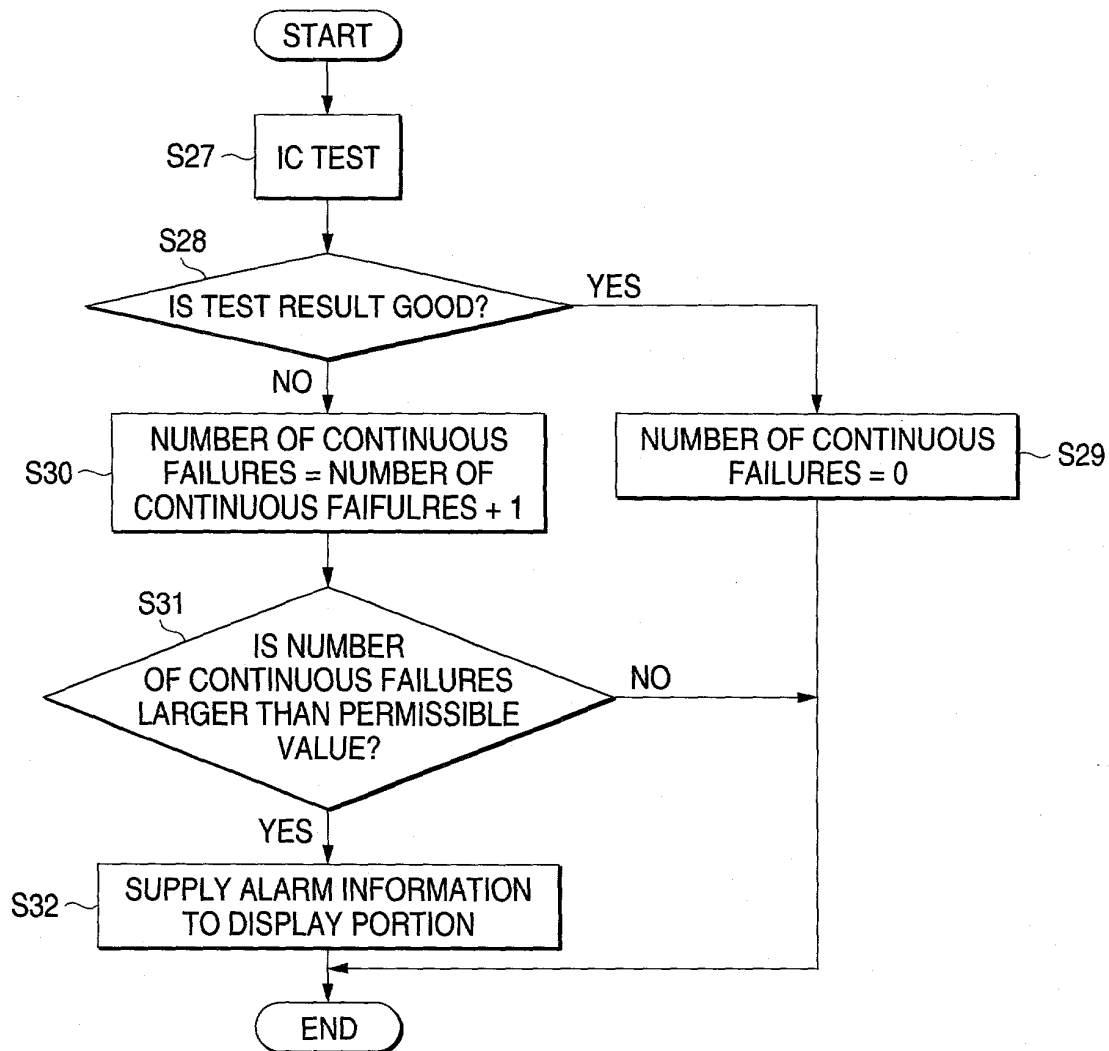


FIG. 5



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FIG. 6



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FIG. 7

